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AF/GP-1765 #8

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TIJ-24816

Masayuki Yasuda

Serial No.: 09/019,087

Art Unit: 1765

Filed: 2/05/98

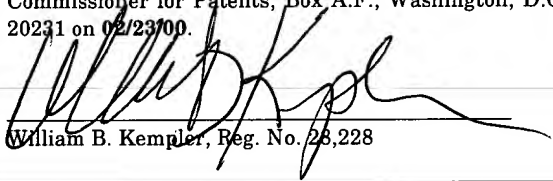
For: MANUFACTURING METHOD OF SEMICONDUCTOR IC DEVICE

NOTICE OF APPEAL

02/23/00

Assistant Commissioner for Patents  
**Box A.F.**  
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A) I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Box A.F., Washington, D.C. 20231 on 02/23/00.

  
William B. Kempler, Reg. No. 28,228

Sir:

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision of the Primary Examiner finally rejecting Claims 1 - 11 in the Office Action dated November 23, 1999.

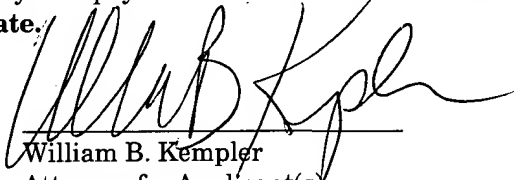
The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection  
☐ was granted on \_\_\_\_\_  
☐ is requested for \_\_\_\_\_ month(s).
2. ☒ A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
3. ☒ Fee \$310.00:  
☐ Not required (*Fee paid in prior appeal*)  
☒ The Commissioner of Patents is hereby authorized to charge any fees which may be required, or credit any overpayment to Account No. 20-0668. **This sheet is enclosed in triplicate.**

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